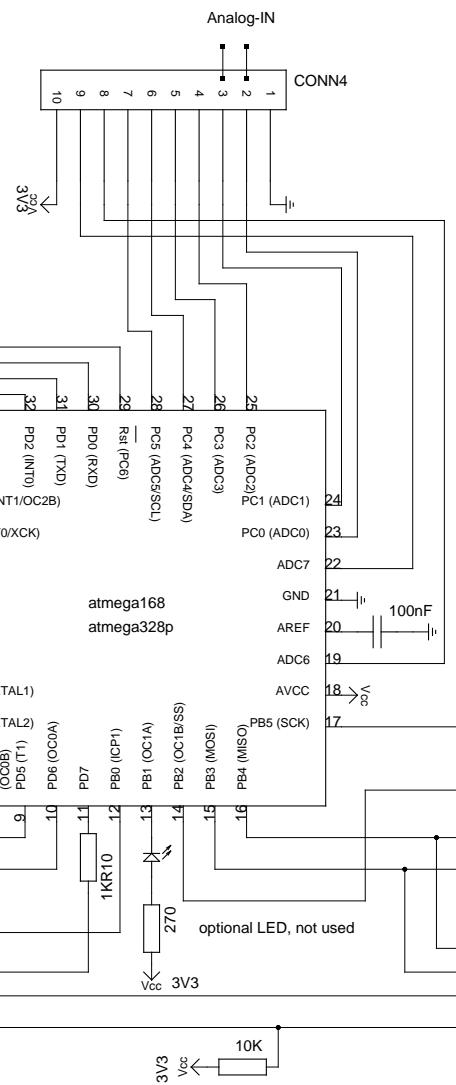
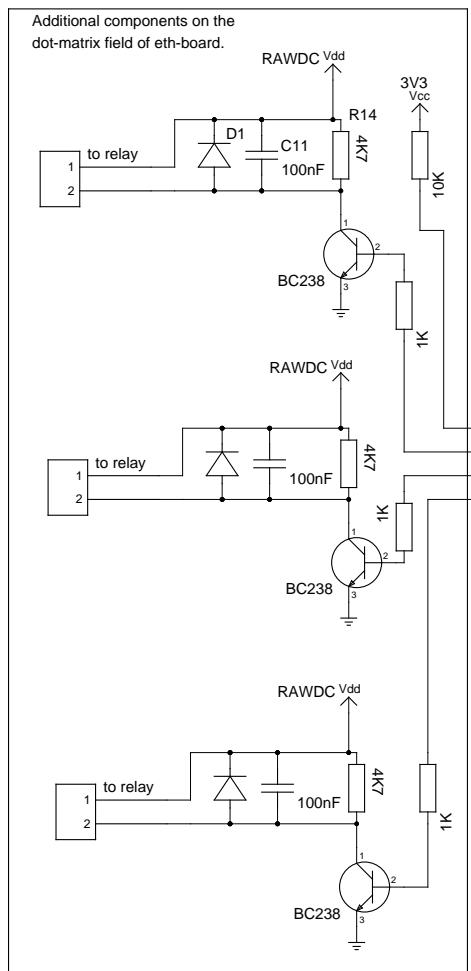


Connection diagram eth_rem_dev_tcp_eqCtl



Digital INPUT lines: PD0 and PD1
Analog INPUT lines: ADC0 and ADC1

Set IP: connect PD2 to GND at power-on
Reset to default IP: PD3 to GND at power-on
The default IP is: 10.0.0.29

The maximum ADC input voltage is 1100 mV and corresponds to a ADC reading of 1023:

ADCreadout=Uin/1.1V * 1023

It is a good idea to add a 470 Ohm resistor (or higher) in series before the input lines (PD0, PD1, ADC0, ADC1) to protect the atmega chip against overvoltage.

